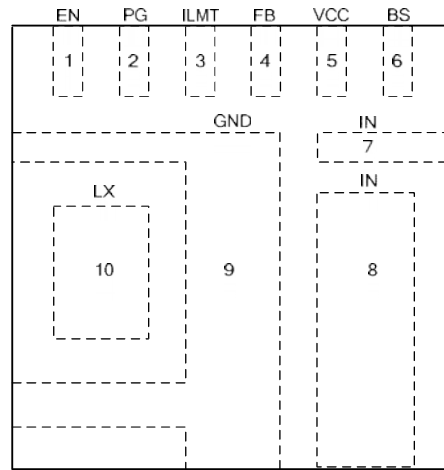




AN_SY8208

Pinout(top view)



(QFN3x3-10)

Top Mark: MR_xyz (Device code: MR, x=year code, y=week code, z= lot number code)

Pin Name	Pin Number	Pin Description
EN	1	Enable control. Pull this pin high to turn on the IC. Do not leave this pin floating.
PG	2	Power good Indicator. Open drain output when the output voltage is within 90% to 120% of regulation point.
ILMT	3	Current limit setting pin. The current limit is set to 8A, 12A or 16A when this pin is pull low, floating or pull high respectively.
FB	4	Output Feedback Pin. Connect this pin to the center point of the output resistor divider (as shown in Figure 1) to program the output voltage: $V_{out}=0.6*(1+R1/R2)$
VCC	5	Internal 3.3V LDO output. Power supply for internal analog circuits and driving circuit. Bypass a capacitor to GND.
BS	6	Boot-Strap Pin. Supply high side gate driver. Decouple this pin to LX pin with 0.1uF ceramic cap.
IN	7,8	Input pin. Decouple this pin to GND pin with at least 10uF ceramic cap
GND	9	Ground pin
LX	10	Inductor pin. Connect this pin to the switching node of inductor